

CLAIMS

1 ✓ 1. A circuit for removing noise on a voltage input
2 line, comprising:
3 a ferrite bead connected in said voltage input line; and
4 a bulk capacitor connected between an output side of said
5 ferrite bead and ground.
6
7 2. The circuit according to claim 1, wherein said
8 capacitor is a D case tantalum bulk capacitor.
9
10 1. The circuit according to claim 2, wherein said
11 capacitor has a resistance of 0.8 ohms.
12
13 1. The circuit according to claim 1, wherein the ferrite
14 bead has a resistance of 0.3 ohms.
15
16 ✓ 5. A voltage supply device comprising:
17 a voltage source including a voltage regulator section
18 producing a voltage output;
19 a ferrite bead connected at one side to said voltage
20 output and forming at another side an output;
21 a capacitor connected between said output and ground;
22 switching regulator noise from said voltage regulator section
23 being removed by said ferrite bead and capacitor.
24
25 1. The voltage supply device according to claim 5,
26 wherein said capacitor is a D case tantalum bulk capacitor.

3 7. The voltage supply device according to claim 5,
4 wherein said capacitor has a resistance of 0.8 ohms.

1 8. The voltage supply device according to claim 5,
2 wherein the ferrite bead has a resistance of 0.3 ohms.

1 ✓9. A method of removing switching regulator noise from a
2 voltage supply line, comprising:
3 connecting a ferrite bead in said voltage input line;
4 connecting a bulk capacitor between an output side of
5 said ferrite bead and ground.

1 10. The method according to claim 9, wherein said
2 capacitor is a D case tantalum bulk capacitor.

1 11. The method according to claim 10, wherein said
2 capacitor has a resistance of 0.8 ohms.

1 12. The method according to claim 9, wherein the ferrite
2 bead has a resistance of 0.3 ohms.

1 ✓13. A voltage source for a clock circuit, comprising:
2 a voltage regulator having an output;
3 a ferrite bead connected to said output of said voltage
4 regulator and having an output;
5 a bulk capacitor connected to said output of said ferrite
6 bead at one side and ground at another side;

7 wher in said f rrit bead and capacitor act to remove
8 switching regulator noise so as to produce an input voltage
9 supply having a reduced switching regulator noise for said
10 clock circuit.

1 14. The voltage source according to claim 13, wherein
2 said capacitor is a D case tantalum bulk capacitor.

1 15. The voltage source according to claim 14, wherein
2 said capacitor has a resistance of 0.8 ohms.

1 16. The voltage source according to claim 13, wherein
2 said the ferrite bead has a resistance of 0.3 ohms.

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